

Winter 2012 Meeting

February 16-17, 2012
RiverPlace Hotel, Portland, OR

AGENDA

THURSDAY, Feb. 16, 2012

12:00-1:00 PM **LUNCH**

1:00-1:30 PM **Opening Remarks and Introductions**

*John Ringo, CDADIC Director
Craig Scott, NSF Evaluator
Marco Gardner, IAB Chair*

CDADIC Progress Reports

MODERATOR: R. Bruce Darling (UW)

1:30-2:50 PM 1. Micro-Power Data Converters

Gabor Temes (OSU)

2. High-Speed ADC in Digital CMOS

George La Rue (WSU)

3. Enabling Metal-Fill-Aware Design of RF/Mixed-Signal Integrated Circuits

Andreas Weisshaar (OSU)

4. Low-Power Low-Jitter All-Digital Frequency Synthesizers

Pavan Kumar Hanumolu (OSU)

2:50-4:00 PM **Poster Session/Networking**

4:00-4:30 PM **NSF/FRP Project Update** - Patrick Chiang and Gabor Temes (OSU)

4:30-5:30 PM **Research Meeting:** Center Research, FY13 & Beyond - An Open Discussion

6:00-8:00 PM **Hosted Social and Dinner**

FRIDAY, Feb. 17, 2012

7:30-8:30 AM **Buffet Breakfast**

8:30-8:45 AM **Reminders**

CDADIC Progress Reports

MODERATOR: Pavan Hanumolu (OSU)

8:45-10:05 AM 5. An Ultra-Low Power PSK Transceiver for Wireless Sensing

Brian Otis (UW)

6. Power Efficient Analog Circuits in CMOS

Un-Ku Moon (OSU)

7. Highly Linear MMICs for Wideband Beamformer Transmitter Applications

Deuk Heo (WSU)

8. Variation Resiliency of Digital Circuits Using Analog-Assisted Techniques

Patrick Yin Chiang (OSU)

10:05-10:45 AM **Poster Session/Networking**

10:45-11:30 AM **Industry/Faculty Project Discussion and IAB Meeting**

11:30-12:30 PM **LUNCH**

END OF MEETING